



- 1x 680K (R1)
- 1x 4K7 (R2)
- 1x 1K (R3)
- 1x 100K (R4)

- 1x 100nF (C1)
- 2x 10uF Electrolytic (C2,C3)
- 2x NPN Transistor (Q1,Q2)

I used a ceramic capacitor for C1 and BC547 for Q1 & Q2  
Q1 is the noise generating transistor so I also put that in a socket

I also chained four outputs to avoid the need for mults in a complex patch

## \* Un! Title: White Noise Generator

This is a very simple design by David Eather, originally published by Silicon Chip  
[http://archive.siliconchip.com.au/cms/A\\_103659/article.html](http://archive.siliconchip.com.au/cms/A_103659/article.html)  
It's easily powered at +12V or +15V from a bipolar supply and has a nice bright output

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