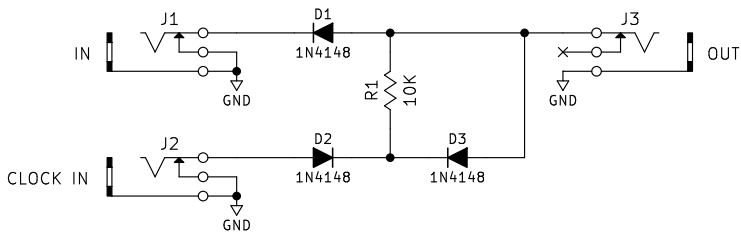


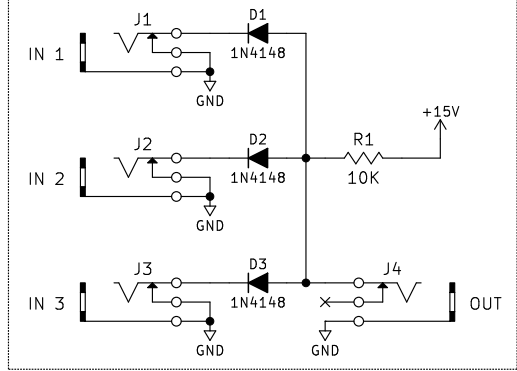
Pseudo Sample & Hold Using AND Logic

Based on the logic circuits found at Doepfer's excellent DIY pages:
www.doepfer.de/DIY/a100_diy.htm

Clock In diode (D2) acts as a half wave rectifier
 The pull-up resistor (R1) connects to Clock In rather than the positive supply rail
 D1 & D3 form the remainder of the AND logic circuit
 Connect N/C inputs to GND



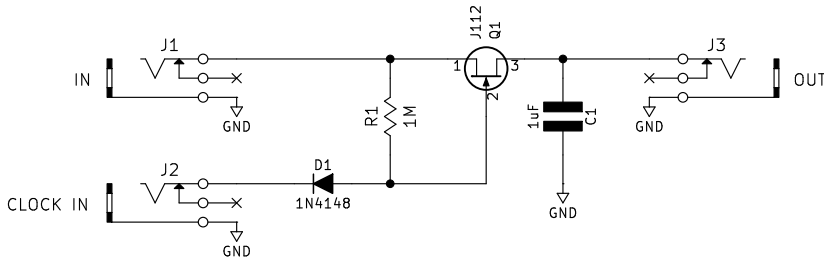
AND Logic



Pseudo Sample & Hold Using A Transistor

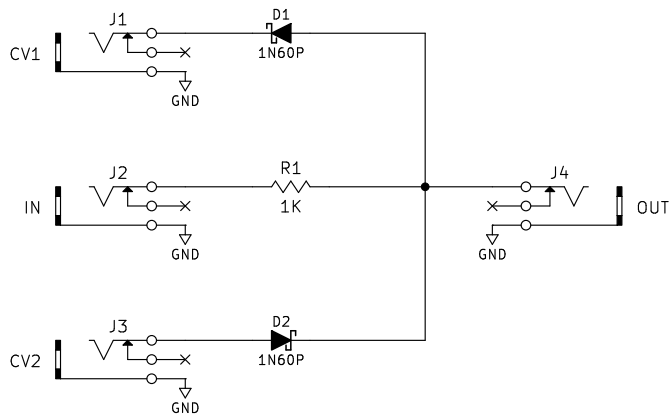
I found this on Sir Yoa's site - <http://siryoa.blogspot.co.uk/2013/01/analogue-bitcrusher.html>
 An n-channel J112 was used here, although any JFET should work
 The diode and resistor are used for stability, they can be omitted from the circuit

The clock signal should have something like a 10% duty cycle (anything like 50% and the input signal will only hold for half of the cycle)



Diode Clamp

Again taken from Doepfer's excellent DIY pages
 Use Schottky diodes (1N60P or BAT42)



The control voltage at CV1 should be greater than that at CV2

* Un! Title: Passive Diode Circuits II

Here are some more very simple passive diode applications