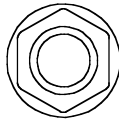
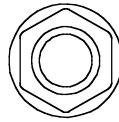


CLK

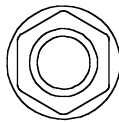


IN

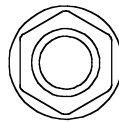


OUT

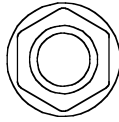
DIODE S&H



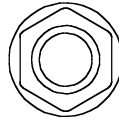
CV1



CV2

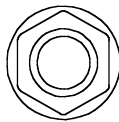


IN

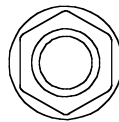


OUT

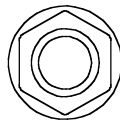
DIODE CLAMP



IN



OUT



CLK



JFET S&H

